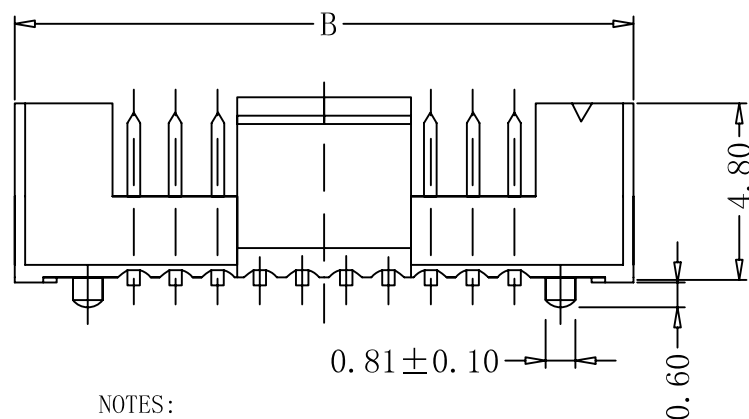
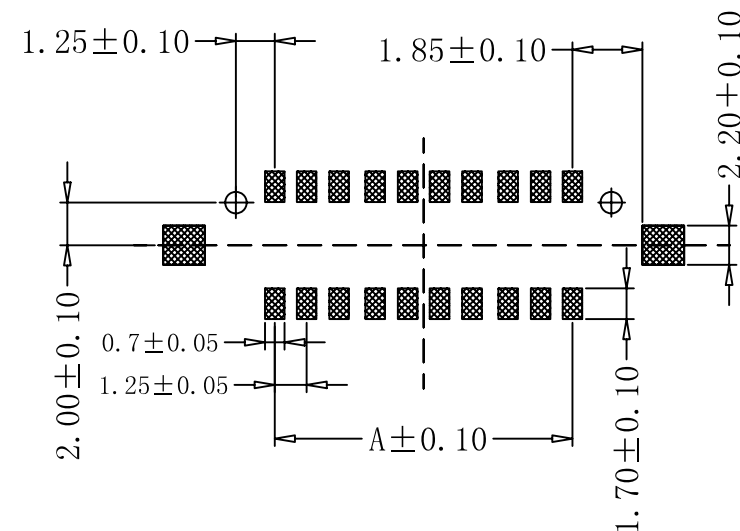
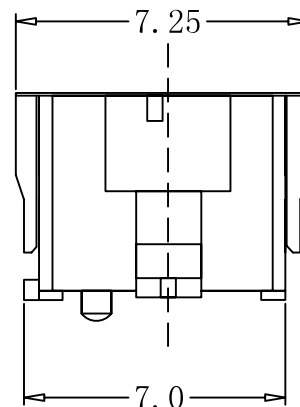
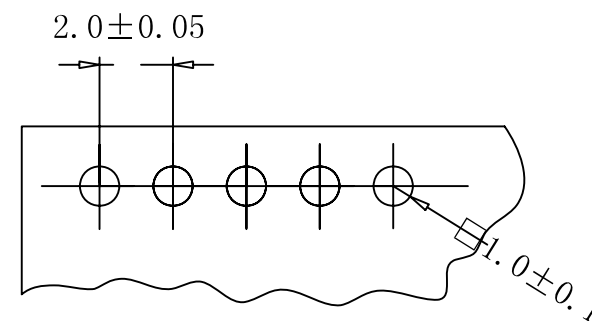


The diagram is a schematic of a photonic receiver. It is symmetrical about a central vertical dashed line. The central part is a large rectangle. On both sides of this central rectangle are identical structures. Each side structure consists of a 4x3 grid of small rectangles, with a larger rectangle to its right. Labels 'CIRCUIT 1' and 'CIRCUIT 2' point to the right-side structures. At the bottom, dimension lines indicate a width of 1.25 for the left half and 0.35 for the right half, with a total width 'A'.

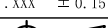


1. INSULATOR:NYLON 6T, UL94V-0
2. CONTACT:BRASS
3. CONTACT FINISH:TIN PLATED
4. LATCH:BRASS
5. LATCH FINISH:TIN PLATED
6. CAP:NYLON 6T UL94V-0
7. SPECIFICATIONS
CURRENT RATING:1A
VOLTAGE RATING:100V AC, DC
TEMPERATURE RANGE:-25° C-85° C
CONTACT RESISTANCE:20mΩ MAX.
INSULATION RESISTANCE: 500mΩ MIN.
WITHSTANDING VOLTAGE:500V AC./minute
8. ORDERING CODE:CNT155-XX 00A



P. C. B. LAYOUT

20P*2	DF13-2*20PB	23. 75	31. 00
15P*2	DF13-2*15PB	17. 50	24. 75
10P*2	DF13-2*10PB	11. 25	18. 50
5P*2	DF13-2*5PB	5. 00	12. 25
CKT/ROW	PART NO.	A	B

 深圳市诚得信电子有限公司 SHENZHEN CHENGDEXIN ELECTRONICS CO.,LTD				TITLE: DF13-2XnAB		
X. ± .X ± 0.30 .XX ± 0.25 .XXX ± 0.15	X. ± .X ± .XX ± .XXX ±	MAT'L \ FINISH \	USE: APPD: CHKD:	PART NO. : DWG NO. :		
	UNITS: mm	Q' TY \	DR: 邓琴	SCALE 1:1	SHEET 1/1	REV. X1